

The technical drawing illustrates the design of a microfluidic chip. The top view shows a cross-section of the chip with a central channel of diameter $\varnothing .310$ MWID [7.87]. The channel is flanked by two side channels, each with a width of $.128$ MIN [3.25]. The chip is mounted on a substrate with a thickness of $.031$ REF [0.79]. The bottom view shows the chip mounted on a tape, with the tape having a thickness of $.031$ REF [0.79]. The tape is labeled "PARTS ON TAPE" and includes a circular feature labeled "D".



6. MUST BE RoHS COMPLIANT

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